



EDAdaptive
COMPUTING, INC.

Enabling System-of-Systems Design Automation™

System-of-Systems Trade-Off Analysis and Optimization
Verification and Validation
On-Board Diagnostics and Self-healing
Security and Anti-Tampering
Rapid Prototyping

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We enable **Acquisition Managers, Primes and System Integrators** develop, verify and sustain **complex, reliable and secure systems and networks**

We have developed several innovative technologies leveraging over \$8M in SBIR investments from DARPA and six other DoD agencies. We are looking for opportunities to transition these to the Warfighter.

EDaptive Computing has a sole-source (Phase III SBIR) Navy IDIQ contract vehicle with a \$45+ million ceiling that can be used by any agency to apply these methods to complex system upgrades and re-certification.

SBA Certified 8 (a) (2005-2014), founded in 1997

Locations:

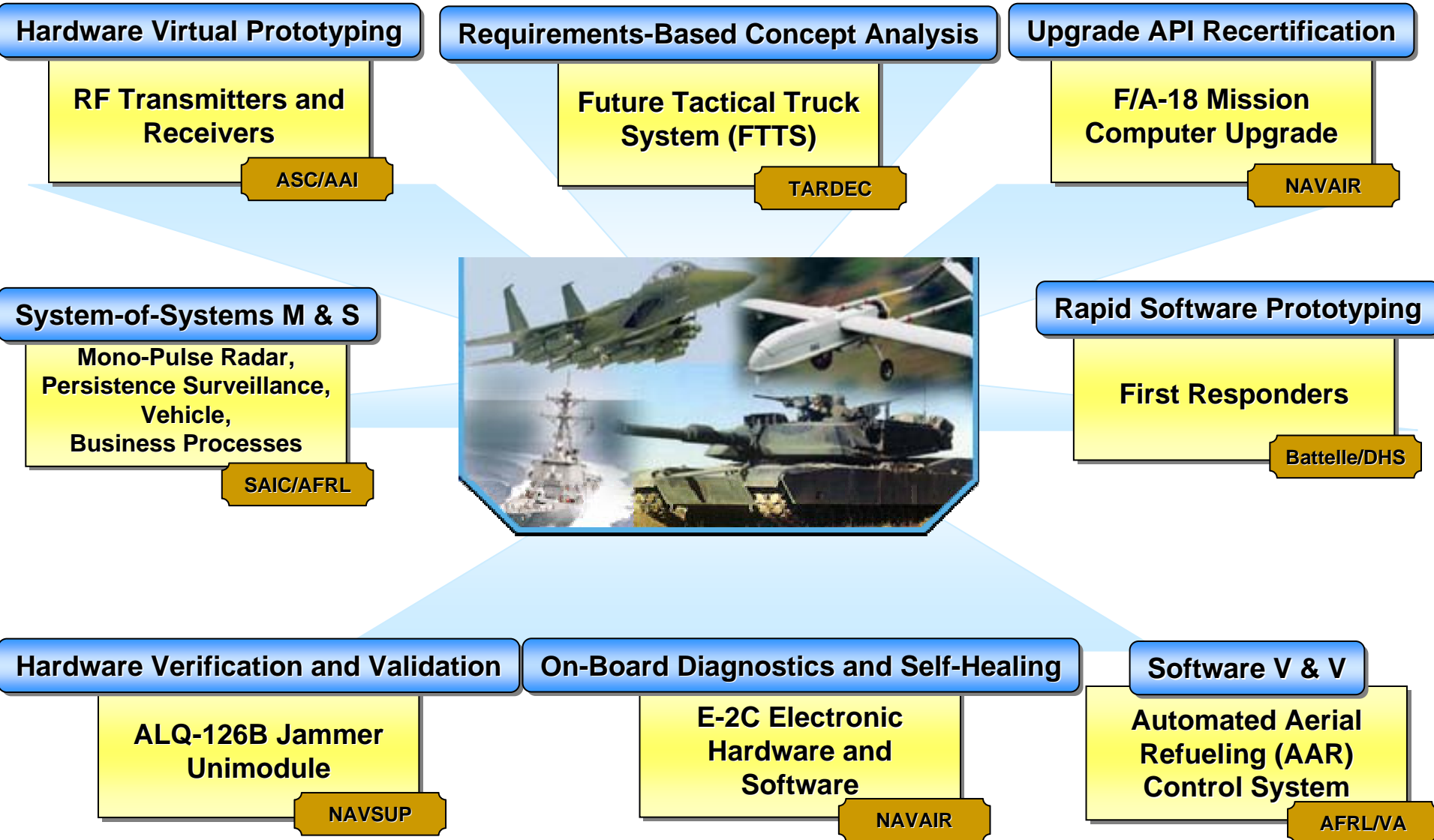
- Dayton, Ohio (Headquarters)
- Satellite Offices (Washington DC, Springfield, OH, San Diego, CA)

Technology Overview

Enabling System-of-Systems Design Automation™

Capability	Enabling Functionality	Applications
<i>System-of-Systems Design</i>	Executable Specification with System Level Design Language, Graphical Tool Suite, Parameterizable model library	What-if analysis of concepts, requirements, designs and upgrades; Analysis of cost, schedule and performance risks; Interoperability analysis; Automating processes and procedures; Business Process Optimization
<i>Rapid Prototyping</i>	FPGA design skills, Reusable software assets	Rapid prototyping of digital electronics; Retargeting legacy systems; Rapid implementation of complex software applications
<i>Verification and Validation</i>	Automated test generation, Formal methods	Acceptance tests; IV&V of new systems and upgrades; Formal verification; Safety and reliability analysis of flight and mission critical systems
<i>On-Board Diagnostics and Self-Healing</i>	Model-based monitoring and self-healing	On-board diagnostics and self-healing; Autonomic computing; Network intrusion detection and prevention
<i>Anti-Tampering and Trust Assurance</i>	Tools and techniques to prevent reverse engineering, and assure trustworthiness of FPGAs	Protect Intellectual Property; Protect against reverse engineering; Assure trustworthiness

Enabling System-of-Systems Design Automation™



Product	Capability	Functionality	Tools
EDASTAR™	System-of-Systems Trade-Off Analysis and Optimization, Verification and Validation	System-of-Systems executable models, verification of safety and correctness properties using formal techniques, automated test generation, trust assurance tests, assertion generation and insertion for safety assurance, development of monitoring models	Syscape™, VectorGen™, SpecSafe™, ModSpec™
EDASHIELD™	Security and Anti-Tampering, On-Board Diagnostics and Self-Healing	Generation and insertion of of Anti-Tamper logic in FPGAs, Protection of software using Co-Processors, monitoring models for diagnostics, intrusion detection and self-healing	SystemCritics™, OCT ² ANE™, SAMURAI™
Professional Services	Model development, Product customization and enhancement, Solution development leveraging reusable assets	Highly qualified and experienced staff and mature processes	Sole-Source IDIQ Phase III SBIR, 8(a), Commercial Catalog, GSA

Transition Milestones/Successes

- Record of SBIR Success
 - 12 Phase II; 2 Phase III awards
 - Over \$8M invested by DARPA, USAF, USN, USA, NASA, MDA, OSD, NSF
- Key Milestones
 - **Successfully completed (3/05) Phase III contract from Air Force**
 - **Awarded (6/04) 5 year Phase III IDIQ contract from NAVAIR with over \$45+M ceiling**
- Partnerships/Collaborations
 - **Lockheed Martin Mentor under Mentor-Protégé project (12/03-12/05)**
 - **Transitioning our SBIR technologies to Springfield Solutions Center under SAIC sponsorship**
 - **Transitioning our SBIR technologies to DHS first responders for assessment of gaps & alternatives under Battelle sponsorship**
 - **Donated software to Wright State University for non-commercial use; Led to formation of EDAdaptive Computing BPM Research Center**
 - **Member of national NACMAST Consortium – Network Attack Characterization Modeling And Simulation Test Center**
 - **Member of NSF I/UCRC Intelligent Maintenance Systems**
 - **Member of DHS Software Assurance Program Working Groups**
 - **Participating in IEEE DASC Working Group for Rosetta Standardization**
- Market Expansion
 - **Transition to DoD and NASA through collaboration with Primes**
 - **Transition to commercial market for Business Process Planning, Optimization**