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DEFINING THE FUTURE

A Day in the Life of a Verification Requirement

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Agenda

- Why Verification
- Overall Process
- Verification Cross-Reference Matrix
- Verification Attributes
- Requirement Samples
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- Benefits
- Summary / Conclusions
- Abstract
- Author Biographies



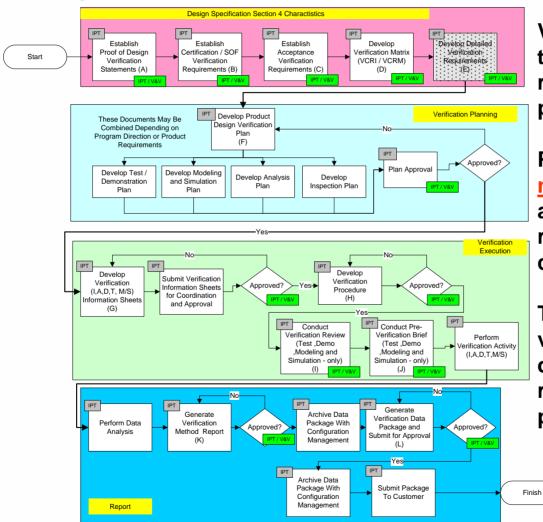
Verification Requirements – What Are They And Why Do We Need Them?

- Verification requirements specify the verification events needed to prove the satisfaction of the product requirements and help to define the verification process and environment
- Verification requirements are necessary for at least two reasons:
 - Existence of verification requirements demonstrates verifiability of product requirements
 - Agreed-to verification requirements define the verification program by which the contractor shows that the product is what the customer needed



A Day in the Life of a Verification Requirement

Product Requirements



Verification events satisfy the verification requirements, <u>NOT</u> the product requirements.

Product requirements are <u>never</u> complete until the associated verification requirements are completed

The culmination of the verification activity of the design requirements results in a verified product.



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Start with Product Requirements

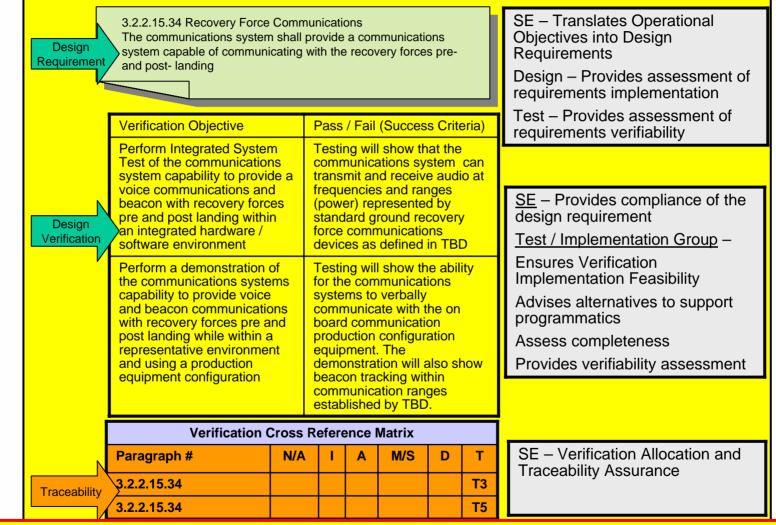
 The verification process begins with authenticated product requirements

Examples

- PR-1:LRU markings
 - The product line-replaceable units shall be marked in accordance with MIL-STD-130M.
- Pr-2: operational availability
 - The product shall have an operational availability (A₀) of 97.5% at IOC.
- Pr-3: Iru accessibility
 - Each product line-replaceable unit shall be able to be removed and replaced without removing any other item or displacing any cables.
- Pr-4:recovery force communication nominal
 - The product shall provide a communications system capable of communicating with the ground command.



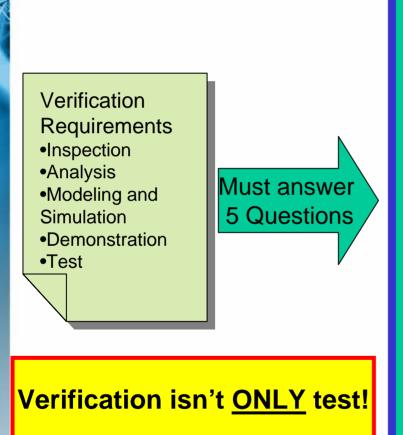
Create Verification Cross-Reference Matrix



Identifying a verification method is necessary, but not sufficient!

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Verification Requirement Attributes



⊠Objective

What is the purpose of this verification?

☑Method

What method do you need performed? What are the verification circumstances (e.g., laboratory, desk-top analysis, flight test)?

MEnvironment

What are the environmental conditions under which the item will be verified?

✓Special Conditions (if necessary)

Are there any unique conditions (e.g., item configurations) necessary for the execution of the verification?

☑Success Criteria

What results are to expected?



Sample Verification Requirements - 1

- VR-1I: compliance of product markings shall be verified by examination of design drawings at the LRU supplier's location prior to the LRU CDR. The inspection will show that each marking on the LRU conforms to MIL-STD-130M.
- Vr-2a: the product operational availability shall be calculated using the results of the governmentaccredited contractor-developed reliability and maintainability analyses performed during the design in conjunction with the design reference missions documented in report xxxx. The analysis will show that the product, in its operational environment, supported with its support equipment and personnel, across all missions, will have an operational availability of at least 97.5%.

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Sample Verification Requirements - 2

- VR-3D: Removal and replacement of all Irus shall be demonstrated on the aircraft to show that each LRU can be removed and replaced without removing any other items or moving any cables.
- Vr-4d: Perform demonstration to provide a communications system capable of communicating with the ground command team while in a representative environment and production configuration. Demonstration will show capability to communicate with recovery forces at TBD distances in the TBD terrain environment.



Sample Verification Requirements - 3

 VR-4T: Prove that the product's communications system is capable of communicating with the ground command team by performing an integrated system test within an integrated hardware/software environment. Testing will show that the product can transmit and receive audio at frequencies represented by standard ground recovery forces communications devices defined in (TBD).

Success Criteria

Verification Objective

Verification Method

Environment

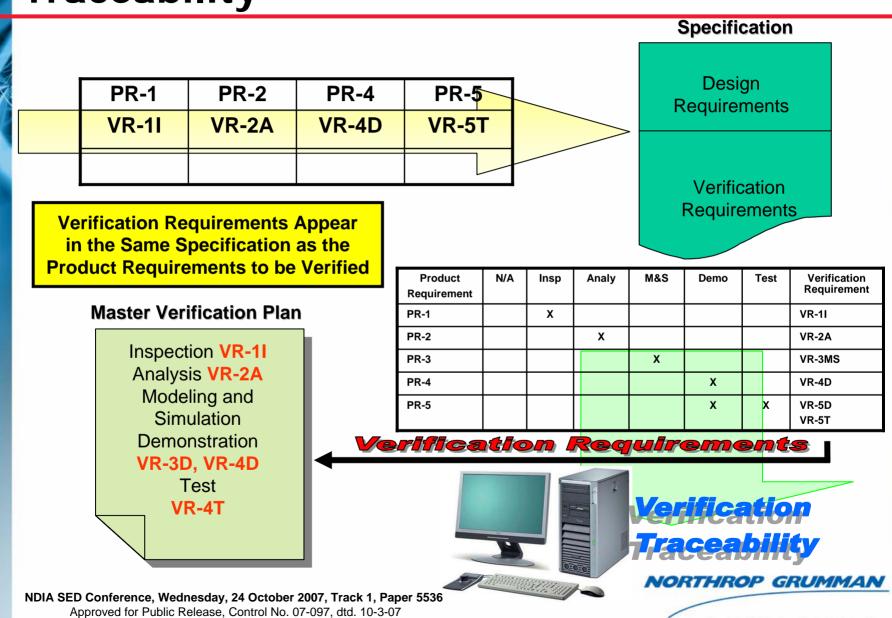
Note – there are no Special Conditions

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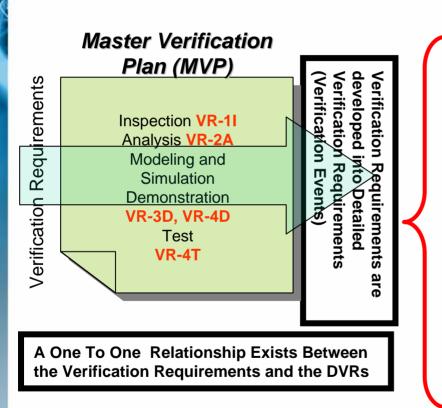
Verification Requirements Flow and Traceability



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Create Detailed Verification Requirements (Verification Events)



Convert verification statements into detailed verification requirements (verification events) by ----

For each verification activity identified in the verification matrix, a detailed description of the activity including:

•Verification configuration & its relationship to production configuration

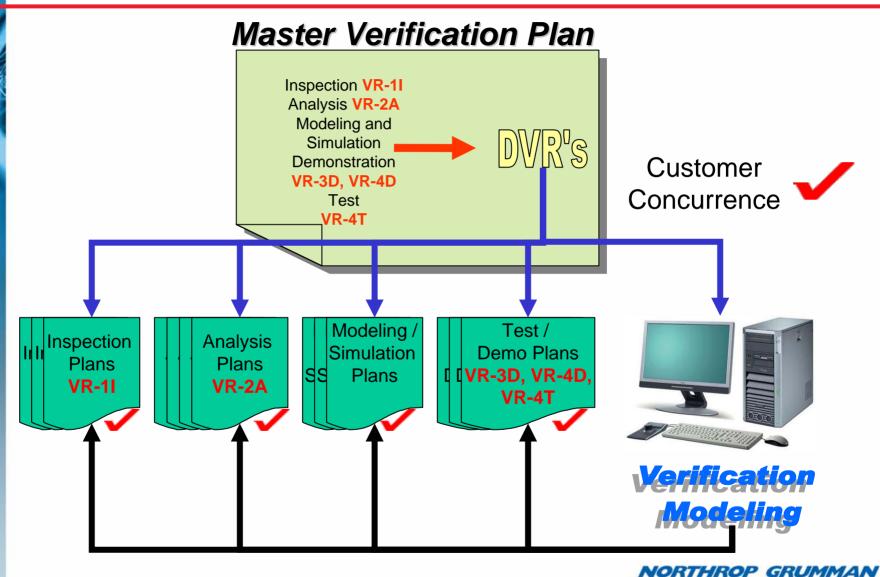
- production configuration
- Associated prerequisites
- Constraints
- Objectives
- Procedures
- •Relevant environmental conditions
- •Pass/fail criteria- and necessary Data Set,
- •Analysis models, if applicable.
- •Sequence if applicable
- •Verification Environment (i.e.; Lab, Flight, Production)



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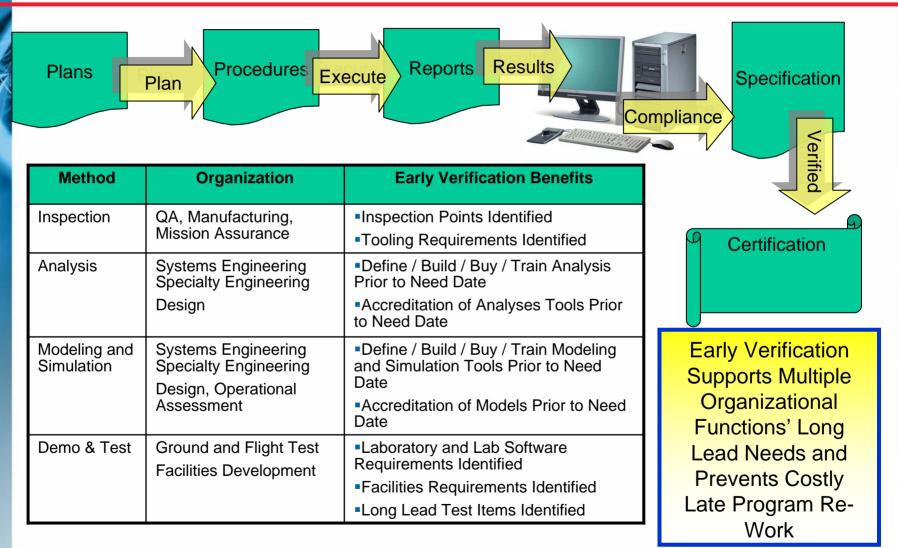
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Master Verification Plan



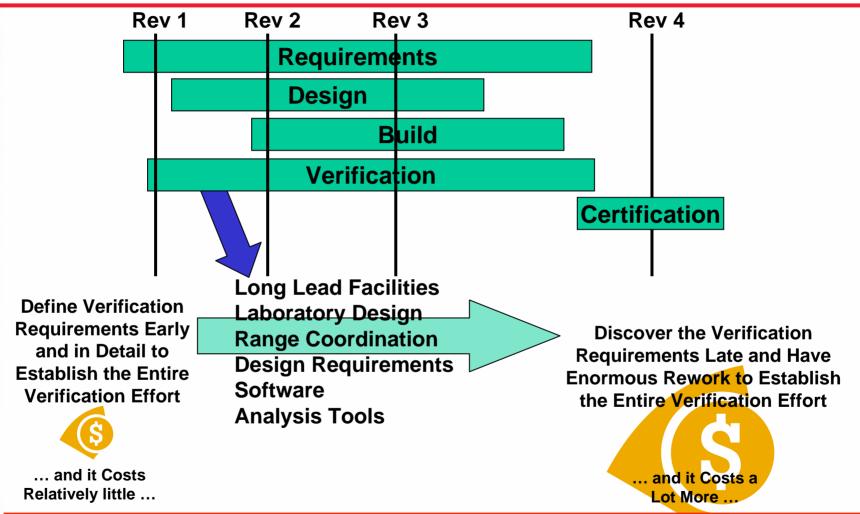
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Verification Execution Flow



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Planning for Verification Execution and Product Verification



Early Verification Is an Effective Cost Avoidance Approach

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Summary and Conclusions

- The verification process begins with authenticated product requirements
- Define verification requirements, not just methods the VCRI is the last thing developed in the specification
- Verification requirements must state the objective, method, environment, and expected results. There may also be special conditions.
- The master verification plan is the guidance for the verification program
- Verification is conducted against the product defined by the title of the specification
- Verification program benefits are not limited to just the systems engineering and test organizations
- Define the verification requirements early to reduce the overall program cost



Abstract

- One measure of the quality of a product requirement is that it be verifiable. Verifiability assessment is one of the exit criteria for the Systems Requirements Review and is necessary for requirement validity. Nomination of one or more verification methods (inspection, analysis, modeling and simulation, demonstration or test) is often taken as the sole evidence of verifiability. A completed Verification Cross Reference Matrix is frequently considered as the final verifiability assessment and responsibility for the remainder of the verification effort is transferred to the test and evaluation and other implementing communities for completion.
- Lessons learned from many Programs have shown that a more robust application of systems engineering should include the requirements engineers (with detailed knowledge of product requirement intent) working with the implementing organizations as the best combination to define the verification requirements. Such definition should include statement of the verification objectives, success criteria and environment. Including this information in the "Quality Assurance" section of the requirements document allows for buyin by the customer well in advance of implementing the verification activities. This information is used by verification personnel to generate one or more verification plans and to develop the detailed verification program. Verification requirements are planned into verification events which are executed using the proper system elements and environments. These verification requirements are key to establishing long lead verification facilities, tools and laboratories. Early definition of these requirements helps prevent facility re-designs and verification re-plans that can cause expensive delays. Finally, verification data analysis is performed, and the information compiled into verification reports certifying system product requirements compliance. This robust verification approach will provide proof of requirements satisfaction. leading to systems that meet the customers' needs at a lower lifecvcle cost.
- This paper describes these concepts and steps in detail and provides examples for a set of generic aircraft requirements.



Author Biographies

- Steve Scukanec has spent over 25 years as an Aerospace Engineer on various complex programs including the B-2, B-2 Long Term Software Support, F-35. With a focus on test and evaluation, Steve has been able to participate in programs from inception to completion. This experience over several programs has provided Steve with a rare understanding of the values of a well executed Verification program as well as the problems caused by the lack of one. His experience as a "requirements generator", "requirements customer", "requirements manager" and verifier gives him insight into the lifecycle of a requirement and a large lessons learned knowledge base.
- Jim van Gaasbeek has 35 years experience analyzing and developing rotary-wing and fixed-wing aircraft, launch vehicles and spacecraft, both in the United States and European defense environments. Beginning as a rotor aeroservoelastician, his career has progressed with experience in constructive and virtual simulation, accident investigation, vehiclemanagement system design and systems engineering, concentrating in risk management and requirements development, management and verification.