



# **Test Perspectives for Architecture**

- Strategic Partnerships
  - Test strategy concepts
  - Architecture views with test strategy insight
- Successful Applications
  - Program #1: Architecture feedback during test planning
  - Program #2: Additional architecture products to support test planning
  - Program #3: Test team engagement with architecture development
- Sample Conversations
  - Define incremental capability
  - Partition functionality

#### Streamline Test Program While Reducing System Complexity

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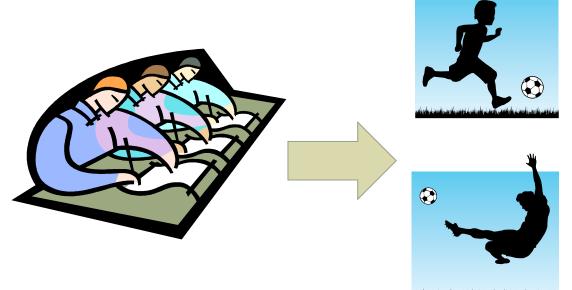
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# **Strategic Partnerships**

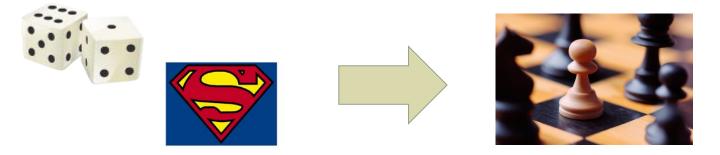
# **Need Test Planning to be Strategic**

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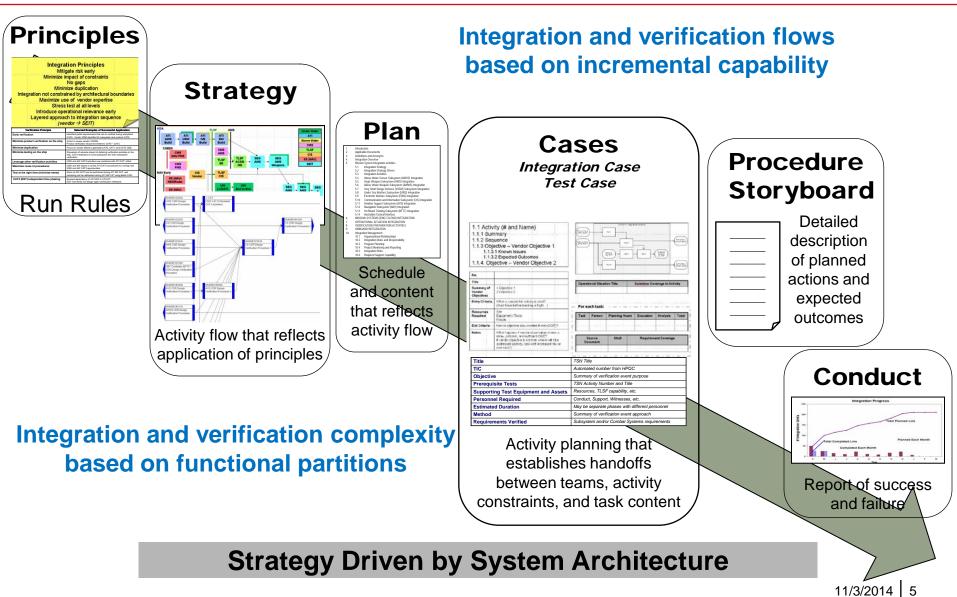
If you compared your integration effort to a soccer team, would it be the way 8 year olds play or the way professionals play?

How much does your verification success depend on SMEs and heroes?



**Transition Luck and Heroics into Strategy** 

### Integration and Verification Strategy Driven by System Architecture



## **Useful Views for Test Strategy**

#### **OV-6c Event Trace Description** Sys A Ship Key Node Aircraft Ship 1 Node A Sys B Msg1() 🗛 A1 Msg2( SV-4 Systems Incremental OV-1s Msg3() **Functionality Description Operational Concept** Msg4() 🗖 A2 F1 F3 Fleet Logistic Support **OV-5b Operational Activity Model** F3' F2 Strike Logistic Suppor Ship 1 Key Node Aircraft Node A Sys B LIST Sys A LIST-0 al Feedback EES-A3 Land Logistic Support SV-5a Operational Activity to A4 ► A1 A6 Systems Function Traceability Matrix 20XX A7 **CV-3** Capability Phasing A2 < A5 Activities or Actions (A1, A2...) Functions (F1, F2. Test Can be a Stakeholder for Architecture

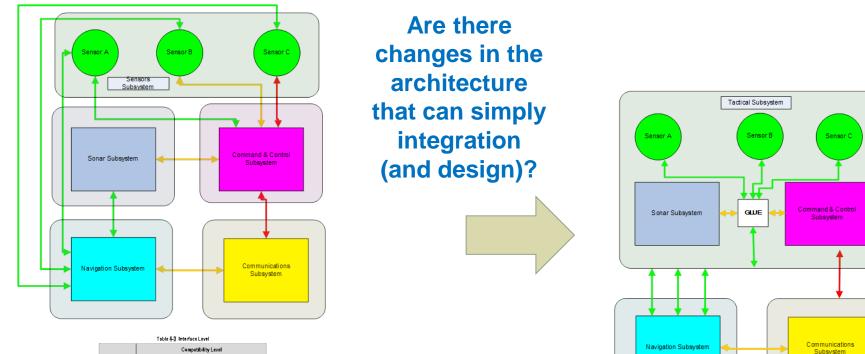


# **Successful Applications**

Program #1: Architecture feedback during test planning Program #2: Additional architecture products to support test planning Program #3: Test team engagement with architecture development

### Program #1 Complexity Feedback

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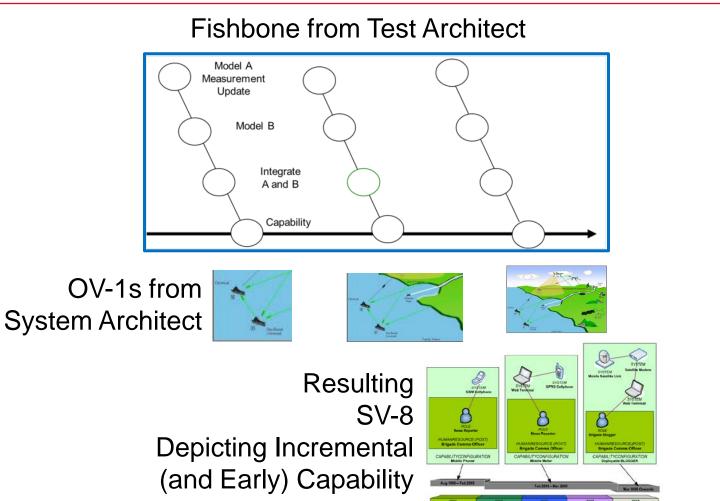




**Interface Ranking** 

#### **Test Architect Can Simplify Interfaces**

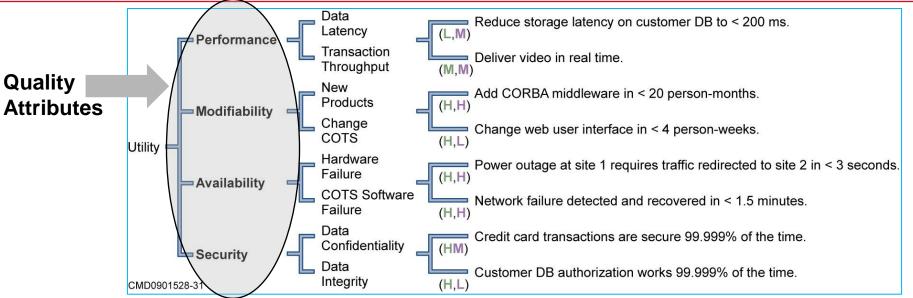
### Program #2 Additional Architecture Products



SV-8 System Evolution

Incremental capability as pursuit win theme

### Program #3: IV&V Tactics for Quality Attributes



Quality Attribute	нพ	SW	IV&V
Interoperability	COTS standard hardware	Open architecture	Low risk interfaces
Usability	Reduce number of monitors	Intuitive workflow processing	Early integration of HMI to influence user acceptance
Scalability	Computing environment with growth	Multi-threading	Focus on integration strategy and capability build up

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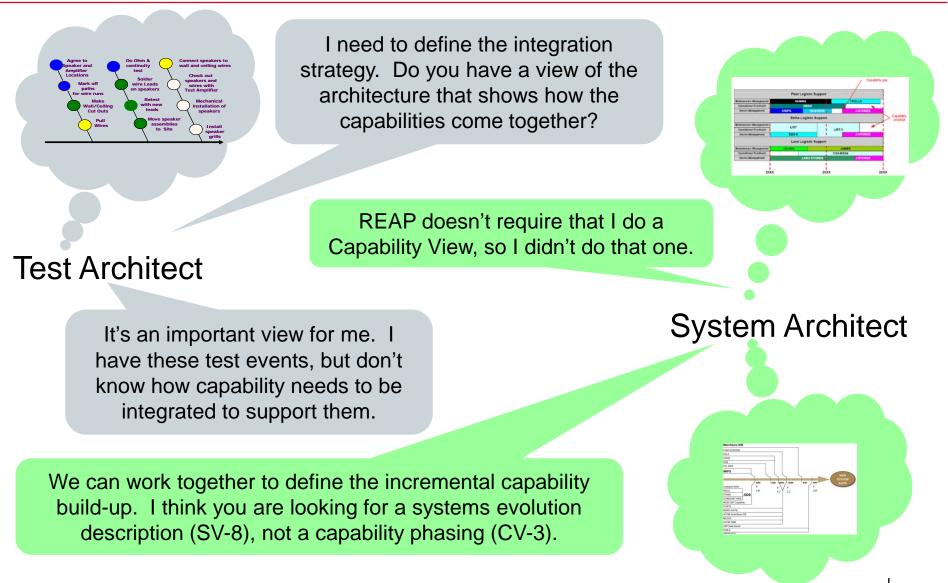
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# Conversations

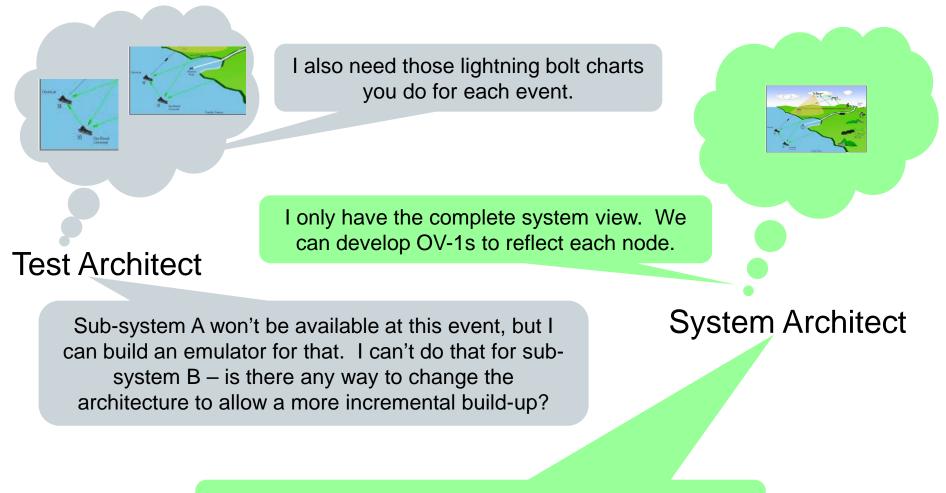
Define incremental capability Partition functionality

### Conversation #1: Capability Build-up



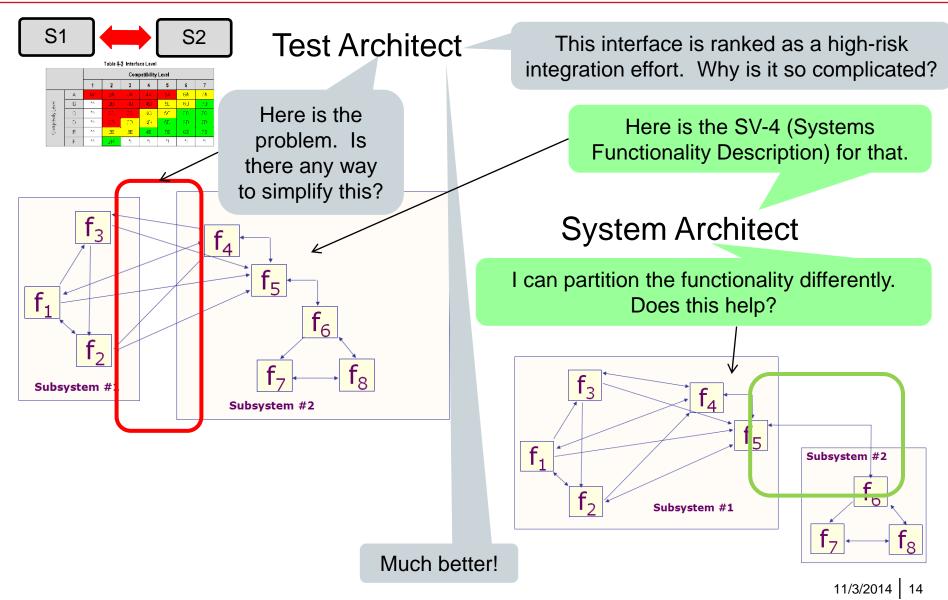
### Conversation #1: Capability Build-up

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I could change the functional allocation. The customer really wants early capability so that would work well.

### Conversation #2: Partitioning Functionality



## Summary

- Test and Architecture interaction is a rich opportunity
  - Design streamlined test program
  - Reducing system complexity.
- Test and Architecture partnerships have been successful
  - Incremental capability
  - Simplified interfaces
  - Test impact on quality attributes

### **Look for Test Perspectives for Architecture Products**

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### Abstract

The intersection between test strategy and architecture development is rich with opportunity to design a streamlined test program while reducing system complexity. The presentation will highlight three successful applications of this overlap showing that the test strategy can provide feedback to simplify the architecture and architecture products can be defined that will define the test strategy. The presentation will then identify test perspectives for architecture development through hypothetical conversations between the test and architecture leads. The examples focus on defining incremental capability and partitioning functionality.

## **Biographies**

- Joe Manas is an Engineering Fellow with Raytheon Company. Over the last 28 years, he has worked within the defense & aerospace industry, 25 years of which has been with Raytheon. Joe has held leadership positions in the disciplines of System Engineering, Software Development and Test & Evaluation across multiple product lines. He holds a B.S. in Electrical Engineering from Worcester Polytechnic Institute, MA.
- Dr. Beth Wilson is a Senior Principal Engineering Fellow who earned her PhD in Electrical Engineering from the University of Rhode Island. Since joining Raytheon in 1983, she has worked as a design engineer, program manager, research scientist, functional manager, and test director on sonar, satellite, and radar programs. She is the NDIA Developmental Test and Evaluation committee chair, and co-chair for the INCOSE and NDIA System Security Engineering committees. Previous assignments have included a character-building deployment to Shemya, Alaska as the Test Director for the Cobra Dane Upgrade. Beth is a Raytheon Certified Architect (RCA), INCOSE Expert Systems Engineering Professional (ESEP), and a Raytheon Certified Six Sigma Expert.